16-wide MIC SIMD bonded interaction kernels

Currently, the bonded interaction calculation code does not support 16-wide SIMD which is essential for utilizing the Intel MIC architecture efficiently.

[ TODO: add what's needed ]

Related issues:
- Related to GROMACS - Feature #1181: Implementing asymmetric offload to MIC (X... Closed
- Related to GROMACS - Feature #1187: Optimized Verlet SIMD kernels for native ... Closed

AFAIK supported in 2016. Close?

History
#1 - 12/06/2013 10:24 AM - Sziárd Páll
- Assignee deleted (Berk Hess)

#2 - 05/13/2014 03:50 PM - Roland Schulz
- Target version changed from 5.0 to 5.x

#3 - 05/10/2016 10:43 PM - Sziárd Páll
- Status changed from New to Feedback wanted
- Target version changed from 5.x to 2016

AFAIK supported in 2016. Close?

#4 - 05/10/2016 10:45 PM - Roland Schulz
- Status changed from Feedback wanted to Closed