### Description
Due to a missing SIMD include file, the Verlet buffer size for rlist was calculated too small by grompp and mdrun with 2-wide simd. This could lead to larger energy drift due particles moving into range during nstlist steps, but the difference in buffer between 4x2 and 4x4 is small (up to 15%). The architectures with 2-wide SIMD are SSE2, SSE4 and AVX128-FMA (all in double precision only) and SPARC64.

### Associated revisions

<table>
<thead>
<tr>
<th>Revision</th>
<th>Date</th>
<th>Author</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>5148ccdc</td>
<td>06/26/2015</td>
<td>Berk Hess</td>
<td>Fixed Verlet buffer issue with 2-wide SIMD</td>
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<td></td>
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<td>The Verlet buffer size for CPUs was always calculated for 4x4. With 2-wide SIMD the estimate should be for 4x2, which results in a slightly larger list buffer. grompp now always sets rlist for a 4x4 list setup; mdrun anyhow redetermines rlist at run time (added a note for this in grompp). Fixes #1757.</td>
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<td>Change-Id: If4bf9ad17b82b22c9d9f7c1dd3f8e66f2314df4</td>
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### History

**#1 - 06/26/2015 09:24 AM** - Gerrit Code Review Bot

Gerrit received a related patchset `1` for Issue [#1757](https://gerrit.gromacs.org/4792).

Uploader: Berk Hess ([hess@kth.se](mailto:hess@kth.se))

Change-Id: If4bf9ad17b82b22c9d9f7c1dd3f8e66f2314df4

**#2 - 06/27/2015 12:48 PM** - Erik Lindahl

... and of those architectures, SPARC64 has not used SIMD for verlet kernels yet. In other words only some double precision builds might have been affected.

**#3 - 06/29/2015 03:20 PM** - Erik Lindahl

- Status changed from New to Fix uploaded

**#4 - 06/29/2015 03:20 PM** - Erik Lindahl

- Status changed from Fix uploaded to Resolved

**#5 - 06/29/2015 03:20 PM** - Erik Lindahl

- Status changed from Resolved to Closed