GROMACS - Bug #2952
Reference SIMD fails with float SIMD width 16
05/14/2019 11:53 AM - Berk Hess

Status: Closed
Priority: Normal
Assignee: Berk Hess
Category: mdrun
Target version: 2019.3
Affected version - any version with reference SIMD
extra info: 
Affected version: 2019.2

Description
The reference SIMD alignment is always set to 8 bytes independent of the reference SIMD width which leads to failure with an alignment error.

Associated revisions
Revision 2795a4ad - 06/05/2019 09:31 AM - Berk Hess
Fix reference SIMD memory alignment
Fixes #2952
Change-id: i86b82c3259986c12c59c0b741df705b4774efe05

History
#1 - 05/14/2019 11:59 AM - Berk Hess
- Status changed from In Progress to Fix uploaded

#2 - 06/05/2019 09:45 AM - Berk Hess
- Status changed from Fix uploaded to Resolved

Applied in changeset 2795a4ad90a670c0bbc859641be0655d70639fd4.

#3 - 06/05/2019 10:07 AM - Paul Bauer
- Status changed from Resolved to Closed